4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



October 8, 2009

Bel Power, Inc., a subsidiary of Bel Fuse, Inc.

### SLIN-03E2Ax

### **RoHS Compliant**

#### Rev.D

#### **Features**

- Wide Input Voltage Range
- Fixed Switching Frequency
- Power Good Signal

Remote On/Off

- Remote Sense

- Ability to Sink and Source Current
- Cost Efficient Open Frame Design
- **Over Temperature Protection**
- **Output Over Current Protection**
- Tunable Loop<sup>™</sup> (a registered trademark of Lineage Power Systems) to Optimize Dynamic output voltage response
- Class 1, Category 2, Non-Isolated DC/DC Converter (refer to IPC-9592)
- Compliant to RoHS EU Directive 2002/95/EC
- Compatible in a Pb-free or SnPb reflow environment
- *UL*\* 60950-1Recognized, *CSA*<sup>†</sup> C22.2 No. 60950-1-03 Certified
- VDE<sup>‡</sup> 0805:2001-12 (EN60950-1) Licensed

#### **Applications**

- Distributed power architectures
- Intermediate bus voltage applications
- Telecommunications equipment
- Servers and storage applications
- Networking equipment
- Industrial equipment



#### Description

The SLIN-03E2Ax power modules are non-isolated dc-dc converters that can deliver up to 3A of output current. These modules operate over a wide range of input voltage ( $V_{IN} = 4.5 \text{Vdc-} 14 \text{Vdc}$ ) and provide a precisely regulated output voltage from 0.59Vdc to 5.5Vdc, programmable via an external resistor. Features include remote On/Off, adjustable output voltage, and over current and over temperature protection. A new feature, the Tunable Loop<sup>TM</sup>, allows the user to optimize the dynamic response of the converter to match the load with reduced amount of output capacitance leading to savings on cost and PWB area.

#### **Part Selection**

Output Voltage	Input Voltage	Max. Output Current			Model Number Active Low	
0.59 Vdc - 5.5 Vdc	4.5 Vdc - 14 Vdc	3 A	91%	SLIN-03E2A0	SLIN-03E2AL	

Notes: Add "G" suffix at the end of the model number to indicate Tray Packaging.

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



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**Part Number Explanation** 

 $\frac{S}{1} \frac{LIN}{2} - \frac{03}{3} \frac{E}{4} \frac{2A}{5} \frac{0}{6}$ 

1---Surface mount

2---Series code

3---Output current (3A)

4--- Wide input voltage range (4.5-14V)

5--- Wide output voltage range (0.59-5.5V)

6--- Enable, active high, change "0" to "L" means active low

#### **Absolute Maximum Ratings**

Parameter	Min	Тур	Max	Unit	Notes
Continuous Input Voltage	-0.3	-	15	V	
Operating Ambient Temperature	-40	-	85	°C	
Storage Temperature	-55	-	125	°C	

**Note:** Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only, functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect the device reliability.

**Input Specifications** 

Parameter	Min	Тур	Max	Unit	Notes
Operating Input Voltage	4.5	-	14	V	
Input Current (full load)	-	-	3.5	Α	V <sub>IN</sub> =4.5V to 14V
Input Current (no load)		4-			
Vo=0.6V Vo=3.3V	-	17 55	-	mA mA	V <sub>IN</sub> =12V, module enabled
Input Stand-by Current	-	1	-	mA	V <sub>IN</sub> = 12.0Vdc, module disabled
Input Reflected Ripple Current (pk-pk)	-	43	-	mA	5Hz to 20MHz, 1μH source impedance; V <sub>IN</sub> =0 to 14V, I <sub>O</sub> = I <sub>Omax</sub> ; See Test Configurations
I <sup>2</sup> t Inrush Current Transient	-	-	1	A <sup>2</sup> s	
Input Ripple Rejection (120Hz)	-	50	-	dB	
Turn-on Threshold	-	-	4.0	V	
Turn-off Threshold	3.6	-	-	V	
Hysteresis	0.4	-	-	V	

#### CAUTION: This converter is not internally fused. An input line fuse must be used in application.

This power module can be used in a wide variety of applications, ranging from simple standalone operation to an integrated part of sophisticated power architecture. To preserve maximum flexibility, internal fusing is not included; however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a fast-acting fuse with a maximum rating of 5A. Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data sheet for further information.

**Note:** Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



October 8, 2009 **Output Specifications** 

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Parame		Mir	า    1	ур	Ma	ax	Į	Jnit	Notes	
Output Voltage S	Set Point	-1.	5	-	+1	.5	%۱	∕o,set	with 0.5% tolerance for external resistor used to set output voltage	
Output Voltage		-3.0	)	-	+3	3.0	%\	∕o,set	Over all operating input voltage, resistive load, and temperature conditions until end of life	
Adjustment Rang	ge <sup>1</sup>	0.5	9	-	5.	.5		V	selected by an external resistor	
Remote Sense F	Range	-		-	0.	.5		V		
Load Regulation	$V_0 \geqslant 2.5V$ $V_0 \le 2.5V$	-		-	1			mV mV	I <sub>O</sub> =I <sub>O, min</sub> to I <sub>O, max</sub>	
Line Regulation	$V_O \geqslant 2.5V$ $V_O \le 2.5V$			- -	0. 1			/o,set mV	V <sub>IN</sub> =V <sub>IN, min</sub> to V <sub>IN, max</sub>	
Temperature Re- (-40°C to +85°C)		-		-	0.	.4	%۱	/o,set		
Ripple and Noise	e (pk-pk)	-		50	11	10		mV	5Hz to 20MHz BW, $V_{IN}=V_{IN, nom}$ and $I_O=I_{O, min}$ to $I_{O, max}$ Co = 0.1 $\mu$ F // 10 $\mu$ F ceramic	
Ripple and Noise	e (rms)	-		20	40 ı		mV	capacitors)		
Output Current R	Range	0		-	3			Α	in either sink or source mode	
Output DC Current Limit		-		-	200		%le	o,max	Hiccup Mode	
Output Short-Circuit Current		-		-	6.7		ı	mA	Vo≤250mV, Hiccup Mode	
ES	Output Capacitance <sup>2</sup> ESR $\geqslant$ 1 m $\Omega$ ESR $\geqslant$ 0.15 m $\Omega$ ESR $\geqslant$ 10 m $\Omega$			- -	10	47 uF 1000 uF 3000 uF		uF	Without the Tunable Loop <sup>TM</sup> With the Tunable Loop <sup>TM</sup> With the Tunable Loop <sup>TM</sup>	
		-		2	-	-	ļ	mS	Case 1: On/Off input is enabled and then input power is applied (delay from instant at which $V_{IN} = V_{IN,  min}$ until Vo =10% of Vo, set)	
Turn-On Delay Time		-		2	-		1	mS	Case 2: Input power is applied for at least one second and then the On/Off input is enabled (delay from instant at which Von/Off is enabled until Vo =10% of Vo, set)	
Output voltage Rise time		-		4	-	-	!	mS	time for Vo to rise from 10% of Vo, set to 90% of Vo, set	
Output voltage overshoot		-		-	3	3	%\	∕o,set	Vin=Vin, min to Vin, max, lo=lo, min to lo, max, With or without maximum external capacitance	
Dynamic Load F	Response									
△V50%~100%	Peak Devia	tion -		+	20	-		mV		
of Max Load	Settling Tin		-	+	- 50				di/dt=10A/us, Vin=Vin,nom, Vo=1.8V,	
△V100%~50% of Max Load	Peak Devia		-	24				mV	Ta=25°C, Co=0	
OI IVIAN LUAU	Settling Tin	ie	-	6	0	-		uS		

- Notes: 1. Some output voltages may not be possible depending on the input voltage.
  2. External capacitors may require using the new Tunable Loop<sup>TM</sup> feature to ensure that the module is stable as well as getting the best transient response (See the Tunable Loop<sup>TM</sup> section for details).
  - 3. Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



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**General Specifications** 

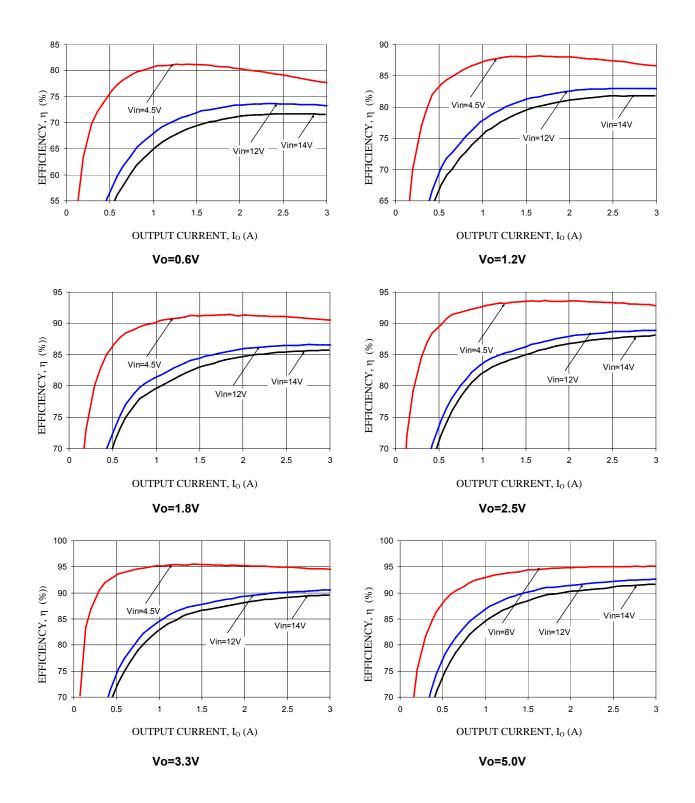
Parameter	Min	Тур	Max	Unit	Notes	
Efficiency						
Vo=0.59V	-	73.3	-	%		
Vo=1.2V	-	82.9	-	%	Vin= 12Vdc, Ta=25°C	
Vo=1.8V	-	86.5	-	%	lo=lo, max , Vo= Vo,set	
Vo=2.5V	-	88.9	-	%	io io, max, vo vo,cot	
Vo=3.3V	-	90.6	-	%		
Vo=5.0V	-	92.6	-	%		
Switching Frequency	-	600	-	kHz		
Over Temperature Protection	-	140	-	°C		
Tracking Accuracy		-	100	mV	Vin min to Vin may: In min to In	
Power-Up: 2V/ms	-				Vin, min to Vin, max; Io, min to Io, max	
Power-Down: 2V/ms	-	-	100	mV	IIIax	
PGOOD (Power Good)					Signal Interface Open Drain	
Output Voltage Limit for PGOOD	90	-	110	%Vo,set	Signal Interface Open Drain, Vsupply ≤ 5Vdc	
Pulldown resistance of PGOOD pin	-	7	50	Ω	vsupply ≤ 5vuc	
Weight	-	1.55	-	g		
MTBF	25,017,068		hours	Calculated Per Bell Core SR-332 (lo=0.8lo, max, Ta=40°C, Telecordia Issue 2 Method 1 Case 3)		
Dimensions						
Inches (L $\times$ W $\times$ H)		8 x 0.48 x 0.2		-		
Millimeters (L $\times$ W $\times$ H)	12.1	9 x 12.19 x 6	5.25			

**Note**: Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



October 8, 2009 Efficiency Data Bel Power, Inc., a subsidiary of Bel Fuse, Inc.



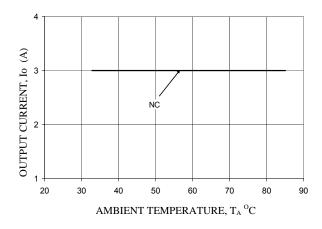
4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs

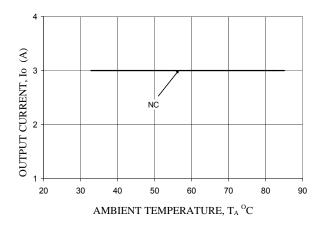


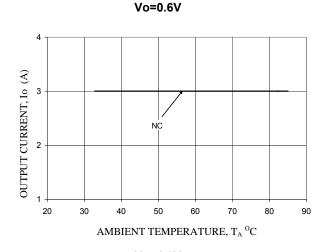
October 8, 2009

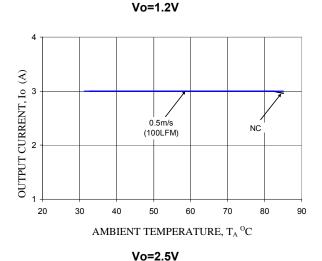
## **Thermal Derating Curves**

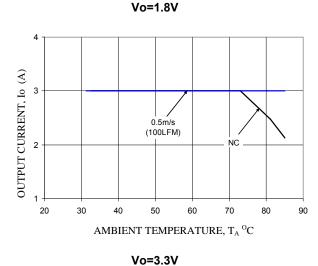
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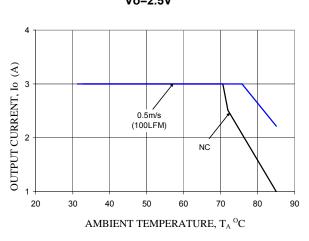










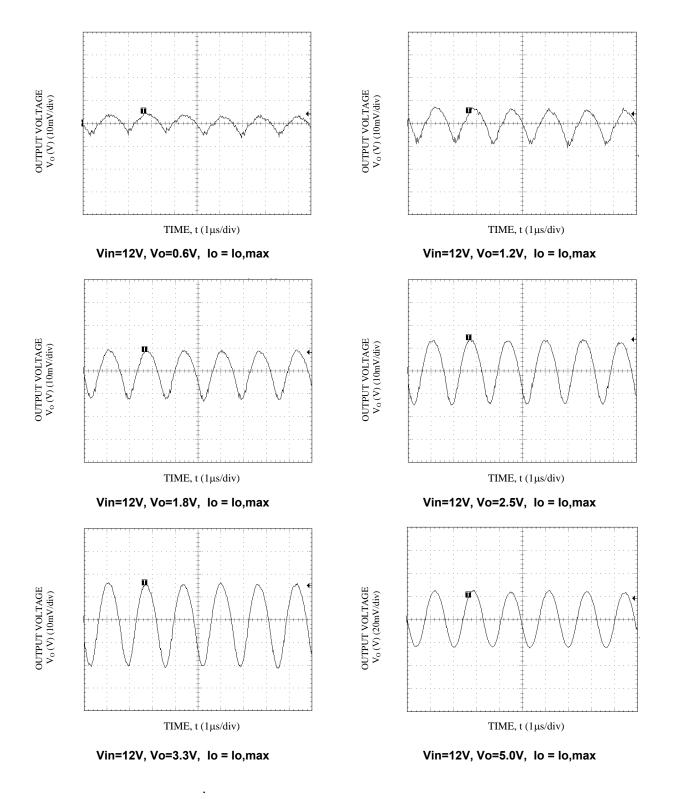


4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



October 8, 2009

## Bel Power, Inc., a subsidiary of Bel Fuse, Inc. **Ripple and Noise Waveforms**



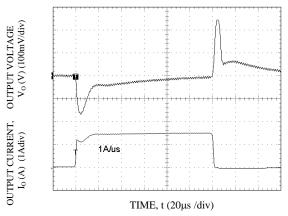
4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



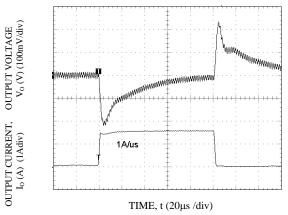
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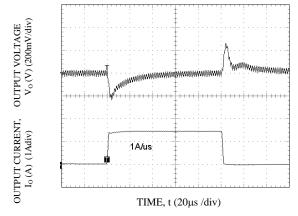
## **Transient Response Waveforms**



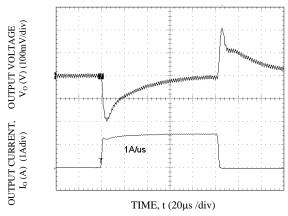
Transient Response to Dynamic Load Change from 0% to 50% to 0%. Vo=0.6V



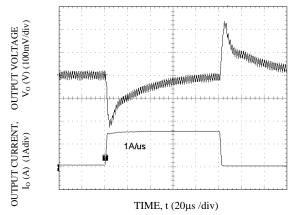
Transient Response to Dynamic Load Change from 0% to 50% to 0%. Vo=1.8V



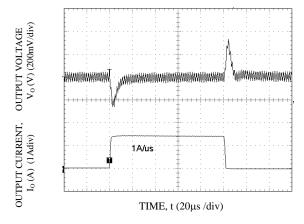
Transient Response to Dynamic Load Change from 0% to 50% to 0%. Vo=3.3V



Transient Response to Dynamic Load Change from 0% to 50% to 0%. Vo=1.2V



Transient Response to Dynamic Load Change from 0% to 50% to 0%. Vo=2.5V



Transient Response to Dynamic Load Change from 0% to 50% to 0%. Vo=5.0V

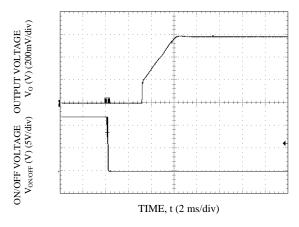
4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



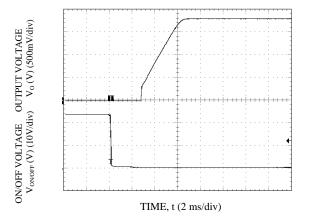
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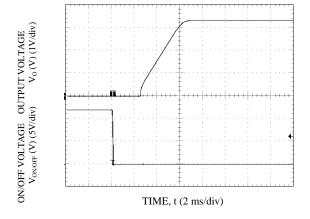
## **Startup Time**



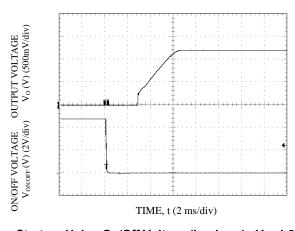
Start-up Using On/Off Voltage (Io = Io,max), Vo=0.6V



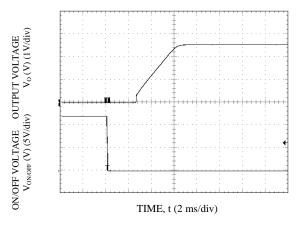
Start-up Using On/Off Voltage (Io = Io,max), Vo=1.8V



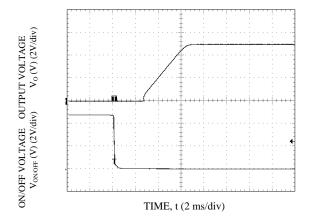
Start-up Using On/Off Voltage (Io = Io,max), Vo=3.3V



Start-up Using On/Off Voltage ( $I_0 = I_{o,max}$ ), Vo=1.2V



Start-up Using On/Off Voltage (Io = Io,max), Vo=2.5V



Start-up Using On/Off Voltage (Io = Io,max), Vo=5.0V

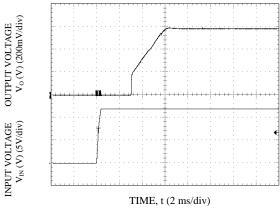
4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



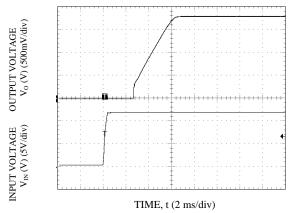
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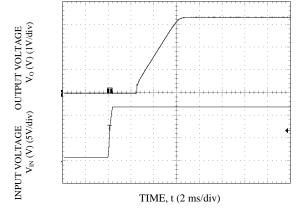
### **Startup Time (continued)**



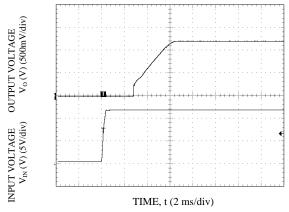
Start-up Using Input Voltage ( $V_{IN}$  = 12V,  $I_{o}$  =  $I_{o,max}$ ), Vo=0.6V



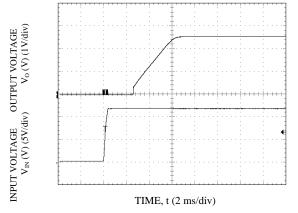
Start-up Using Input Voltage ( $V_{IN} = 12V$ ,  $I_0 = I_{o,max}$ ), Vo=1.8V



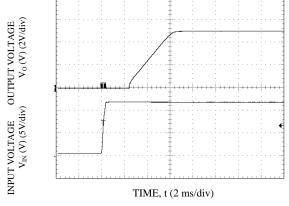
Start-up Using Input Voltage ( $V_{IN}$  = 12V,  $I_0$  =  $I_{o,max}$ ), Vo=3.3V



Start-up Using Input Voltage ( $V_{IN}$  = 12V,  $I_o$  =  $I_{o,max}$ ), Vo=1.2V



Start-up Using Input Voltage ( $V_{IN}$  = 12V,  $I_o$  =  $I_{o,max}$ ), Vo=2.5V



Start-up Using Input Voltage ( $V_{IN}$  = 12V,  $I_o$  =  $I_{o,max}$ ), Vo=5.0V

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs

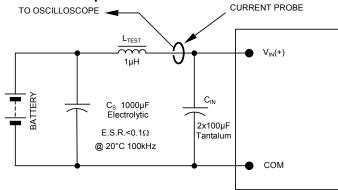


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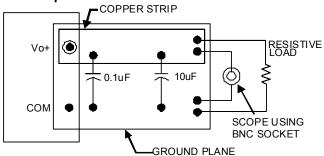
### **Test Configurations**

#### Input Reflected Ripple Current Test Setup.



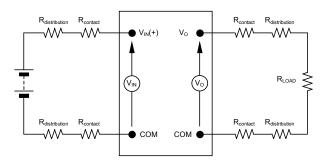
NOTE: Measure input reflected ripple current with a simulated source inductance (L<sub>TEST</sub>) of 1µH. Capacitor C<sub>S</sub> offsets possible battery impedance. Measure current as shown above.

#### **Output Ripple and Noise Test Setup**



NOTE: All voltage measurements to be taken at the module terminals, as shown above. If sockets are used then Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.

### **Output Voltage and Efficiency Test Setup**



NOTE: All voltage measurements to be taken at the module terminals, as shown above. If sockets are used then Kelvin connections are required at the module terminals to avoid measurement errors due to socket contact resistance.

Efficiency 
$$\eta = \frac{V_0. I_0}{V_{IN. IIN}} \times 100 \%$$

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



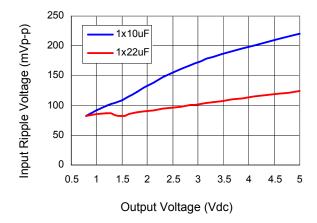
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#### **Input Filtering**

The SLIN-03E2Ax module should be connected to a low ac-impedance source. A highly inductive source can affect the stability of the module. Input capacitance must be placed directly adjacent to the input pin of the module, to minimize input ripple voltage and ensure module stability.

To minimize input voltage ripple, ceramic capacitors are recommended at the input of the module. Below figure shows the input ripple voltage for various output voltages at 3A of load current with 1x10  $\mu$ F or 1x22  $\mu$ F ceramic capacitors and an input of 12V.



### **Output Filtering**

The SLIN-03E2Ax modules are designed for low output ripple voltage and will meet the maximum output ripple specification with 0.1  $\mu$ F ceramic and 10  $\mu$ F ceramic capacitors at the output of the module. However, additional output filtering may be required by the system designer for a number of reasons. First, there may be a need to further reduce the output ripple and noise of the module. Second, the dynamic response characteristics may need to be customized to a particular load step change.

To reduce the output ripple and improve the dynamic response to a step load change, additional capacitance at the output can be used. Low ESR polymer and ceramic capacitors are recommended to improve the dynamic response of the module. Below figure provides output ripple information for different external capacitance values at various Vo and for a load current of 3A. For stable operation of the module, limit the capacitance to less than the maximum output capacitance as specified in the electrical specification table. Optimal performance of the module can be achieved by using the Tunable Loop<sup>TM</sup> feature described later in this data sheet.

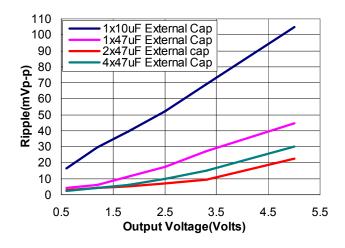
4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



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## **Output Filtering (continued)**



**Note:** Output ripple voltage for various output voltages with external 1x10 μF, 1x47 μF, 2x47 μF or 4x47 μF ceramic capacitors at the output (3A load). Input voltage is 12V.

#### **Safety Considerations**

For safety agency approval the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standards, i.e., UL 60950-1, CSA C22.2 No. 60950-1-03, and VDE 0850:2001-12 (EN60950-1) Licensed.

For the converter output to be considered meeting the requirements of safety extra-low voltage (SELV), the input must meet SELV requirements. The power module has extra-low voltage (ELV) outputs when all inputs are ELV. The input to these units is to be provided with a fast-acting fuse with a maximum rating of 6A in the positive input lead.

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



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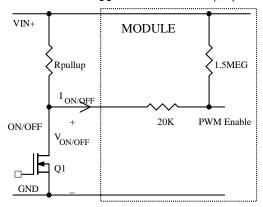
## Bel Power, Inc., a subsidiary of Bel Fuse, Inc.

#### Remote On/Off

Parameter	Min	Тур	Max	Unit	Notes	
Signal Low (Unit On)	Active Low	-0.2	-	0.3	V	The remote on/off pin open, Unit off.
Signal High (Unit Off)	Active Low	3.5	-	Vin,max	V	The remote on/on pin open, onit on.
Signal Low (Unit Off)	- Active High	-0.3	-	0.8	V	The remote en/off pin enen Unit en
Signal High (Unit On)		3.5	-	Vin,max	V	The remote on/off pin open, Unit on.

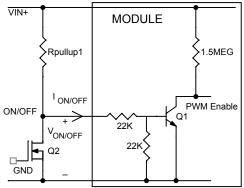
The SLIN-03E2Ax modules feature an On/Off pin for remote On/Off operation. Two On/Off logic options are available. In the Positive Logic On/Off option, the module turns ON during a logic High on the On/Off pin and turns OFF during a logic Low. With the Negative Logic On/Off option, the module turns OFF during logic High and ON during logic Low. The On/Off signal is always referenced to ground. For either On/Off logic option, leaving the On/Off pin disconnected will turn the module ON when input voltage is present.

For positive logic modules, the circuit configuration for using the On/Off pin is shown in below figure. When the external transistor Q1 is in the OFF state, the internal PWM Enable signal is pulled high through an internal  $1.5 M\Omega$  resistor and the external pullup resistor and the module is ON. When transistor Q1 is turned ON, the On/Off pin is pulled low and the module is OFF. A suggested value for  $R_{pullup}$  is  $20 k\Omega$ .



Circuit configuration for using positive On/Off logic

For negative logic On/Off modules, the circuit configuration is shown below. The On/Off pin should be pulled high with an external pull-up resistor (suggested value for the 4.5V to 14V input range is 20Kohms). When transistor Q2 is in the OFF state, the On/Off pin is pulled high, transistor Q1 is turned ON and the module is OFF. To turn the module ON, Q2 is turned ON pulling the On/Off pin low, turning transistor Q1 OFF resulting in the PWM Enable pin going high.



Circuit configuration for using negative On/Off logic

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



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#### **Over Current Protection**

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry and can endure current limiting continuously. At the point of current-limit inception, the unit enters hiccup mode. The unit operates normally once the output current is brought back into its specified range.

#### **Over Temperature Protection**

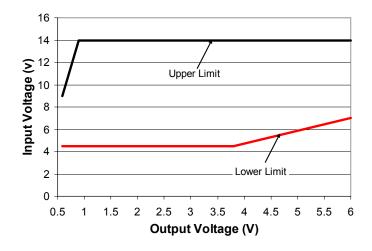
To provide protection in a fault condition, the unit is equipped with a thermal shutdown circuit. The unit will shutdown if the over temperature threshold of 140°C is exceeded at the thermal reference point Tref. The thermal shutdown is not intended as a guarantee that the unit will survive temperatures beyond its rating. Once the unit goes into thermal shutdown it will then wait to cool before attempting to restart.

#### Input Under-voltage Lockout

At input voltages below the input under voltage lockout limit, the module operation is disabled. The module will begin to operate at an input voltage above the under voltage lockout turn-on threshold.

#### **Output Voltage Programming**

The output voltage of the SLIN-03E2Ax module can be programmed to any voltage from 0.59dc to 5.5Vdc by connecting a resistor between the Trim and GND pins of the module. Certain restrictions apply on the output voltage set point depending on the input voltage. These are shown in the Output Voltage vs. Input Voltage Set Point Area plot in below. The Upper Limit curve shows that for output voltages of 0.9V and lower, the input voltage must be lower than the maximum of 14V. The Lower Limit curve shows that for output voltages of 3.8V and higher, the input voltage needs to be larger than the minimum of 4.5V.



Output Voltage vs. Input Voltage Set Point Area plot showing limits where the output voltage can be set for different input voltages.

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



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## **Output Trim Equations**

Without an external resistor between Trim and GND pins, the output of the module will be 0.59Vdc. To calculate the value of the trim resistor, *Rtrim*, for a desired output voltage, use the following equation:

$$Rtrim = \left[\frac{5.91}{(Vo - 0.591)}\right] k\Omega$$

Rtrim is the external resistor in  $k\Omega$  Vo is the desired output voltage.

Table 1 provides Rtrim values required for some common output voltages.

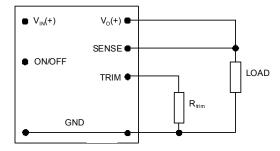
Table 1

V <sub>O, set</sub> (V)	Rtrim
	<i>(</i> ΚΩ)
0.6	656.7
1.0	14.45
1.2	9.704
1.5	6.502
1.8	4.888
2.5	3.096
3.3	2.182
5.0	1.340

By using a  $\pm 0.5\%$  tolerance trim resistor with a TC of  $\pm 100$ ppm, a set point tolerance of  $\pm 1.5\%$  can be achieved as specified in the electrical specification.

#### **Remote Sense**

The SLIN-03E2Ax modules have a Remote Sense feature to minimize the effects of distribution losses by regulating the voltage at the SENSE pin. The voltage between the SENSE pin and VOUT pin must not exceed 0.5V. Note that the output voltage of the module cannot exceed the specified maximum value. This includes the voltage drop between the SENSE and Vout pins. When the Remote Sense feature is not being used, connect the SENSE pin to the VOUT pin.



Circuit configuration for programming output voltage using an external resistor

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs

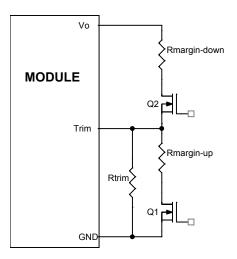


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## **Voltage Margining**

Output voltage margining can be implemented in the SLIN-03E2Ax modules by connecting a resistor,  $R_{\text{margin-up}}$ , from the Trim pin to the ground pin for margining-up the output voltage and by connecting a resistor,  $R_{\text{margin-down}}$ , from the Trim pin to output pin for margining-down.



Circuit Configuration for margining Output voltage

#### **Monotonic Start-up and Shutdown**

The SLIN-03E2Ax modules have monotonic start-up and shutdown behavior for any combination of rated input voltage, output current and operating temperature range.

#### **Startup into Pre-biased Output**

The SLIN-03E2Ax modules can start into a prebiased output as long as the prebias voltage is 0.5V less than the set output voltage.

#### **Power Good**

The SLIN-03E2Ax modules provide a Power Good (PGOOD) signal that is implemented with an open-drain output to indicate that the output voltage is within the regulation limits of the power module. The PGOOD signal will be de-asserted to a low state if any condition such as over temperature, over current or loss of regulation occurs that would result in the output voltage going  $\pm 10\%$  outside the set-point value. The PGOOD terminal should be connected through a pull-up resistor (suggested value  $100\mathrm{K}\Omega$ ) to a source of 5VDC or lower.

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



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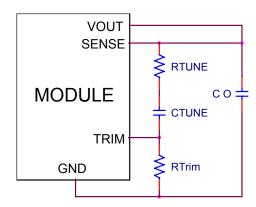


# Tunable Loop<sup>™</sup>

The SLIN-03E2Ax modules have a new feature that optimizes transient response of the module called Tunable  $Loop^{TM}$ .

External capacitors are usually added to the output of the module for two reasons: to reduce output ripple and noise and to reduce output voltage deviations from the steady-state value in the presence of dynamic load current changes. Adding external capacitance however affects the voltage control loop of the module, typically causing the loop to slow down with sluggish response. Larger values of external capacitance could also cause the module to become unstable.

The Tunable Loop<sup>TM</sup> allows the user to externally adjust the voltage control loop to match the filter network connected to the output of the module. The Tunable Loop<sup>TM</sup> is implemented by connecting a series R-C between the SENSE and TRIM pins of the module, as shown below. This R-C allows the user to externally adjust the voltage loop feedback compensation of the module.



Circuit diagram showing connection of  $R_{\text{TUNE}}$  and  $C_{\text{TUNE}}$  to tune the control loop of the module

Recommended values of  $R_{\text{TUNE}}$  and  $C_{\text{TUNE}}$  for different output capacitor combinations are given in below Tables 2 and 3. Table 2 shows the recommended values of  $R_{\text{TUNE}}$  and  $C_{\text{TUNE}}$  for different values of ceramic output capacitors up to 470uF that might be needed for an application to meet output ripple and noise requirements. Selecting  $R_{\text{TUNE}}$  and  $C_{\text{TUNE}}$  according to Table 2 will ensure stable operation of the module.

In applications with tight output voltage limits in the presence of dynamic current loading, additional output capacitance will be required. Table 3 lists recommended values of  $R_{\text{TUNE}}$  and  $C_{\text{TUNE}}$  in order to meet 2% output voltage deviation limits for some common output voltages in the presence of a 1.5A to 3A step change (50% of full load), with an input voltage of 12V.

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



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# Tunable Loop<sup>™</sup> (continued)

**Table 2**.General recommended values of  $R_{\text{TUNE}}$  and  $C_{\text{TUNE}}$  for Vin=12V and various external ceramic capacitor combinations.

Со	1x47μF	2x47μF	4x47μF	6x47μF	10x47μF
R <sub>TUNE</sub>	270	180	100	75	75
C <sub>TUNE</sub>	2200pF	4700pF	18nF	18nF	22nF

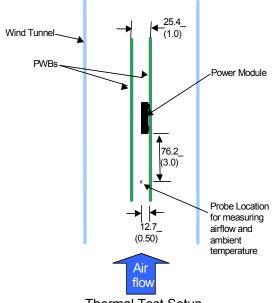
**Table 3**.Recommended values of  $R_{\text{TUNE}}$  and  $C_{\text{TUNE}}$  to obtain transient deviation of  $\leq$ 2% of Vout for a 1.5A step load with Vin=12V.

Vo	5V	3.3V	2.5V	1.8V	1.2V	0.6V
Co	1x22μF	1x47μF	2x47μF	2x47μF	3x47μF	1x47μF+330μF Polymer
R <sub>TUNE</sub>	270	270	180	150	150	100
C <sub>TUNE</sub>	820pF	2200pF	4700pF	4700pF	10nF	15nF
ΔV	100mV	64mV	37mV	36mV	22mV	12mV

#### **Thermal Considerations**

Power modules operate in a variety of thermal environments; however, sufficient cooling should always be provided to help ensure reliable operation.

Considerations include ambient temperature, airflow, module power dissipation, and the need for increased reliability. A reduction in the operating temperature of the module will result in an increase in reliability. The thermal data presented here is based on physical measurements taken in a wind tunnel.



Thermal Test Setup

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs

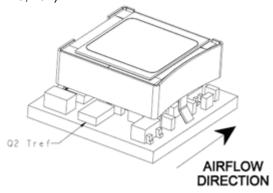


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## **Thermal Considerations (continued)**

The thermal reference points,  $T_{ref}$  used in the specifications are also shown below. For reliable operation the temperatures at these points should not exceed  $115^{\circ}$ C. The output power of the module should not exceed the rated power of the module (Vo,set x lo,max).



Preferred airflow direction and location of hot-spot of the module (Tref).

### **Example Application Circuit**

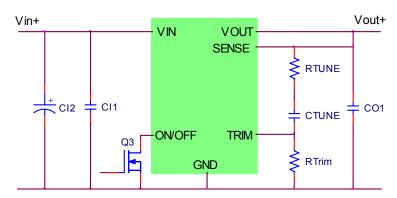
#### Requirements:

Vin: 12V Vout: 1.8V

lout: 2.25A max., worst case load transient is from 1.5A to 2.25A

ΔVout: 1.5% of Vout (27mV) for worst case load transient

Vin, ripple 1.5% of Vin (50mV, p-p)



CI1 10µF/16V ceramic capacitor (e.g. Murata GRM Series)

CI2 47µF/16V bulk electrolytic

CO1  $2 \times 47 \mu F/6.3V$  ceramic capacitor (e.g. TDK C Series) CTune 4.7nF ceramic capacitor (can be 1206, 0805 or 0603 size) RTune 180 ohms SMT resistor (can be 1206, 0805 or 0603 size)

RTrim 4.87k $\Omega$  SMT resistor (can be 1206, 0805 or 0603 size, recommended tolerance of 0.1%)

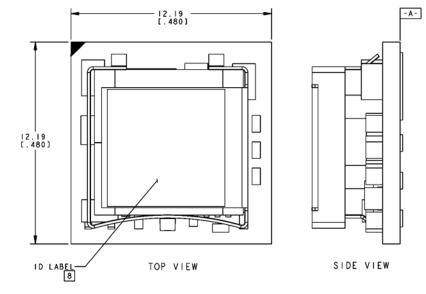
4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs

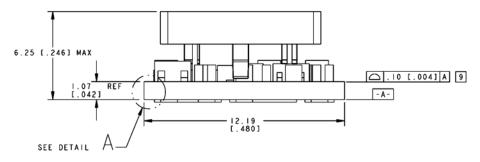


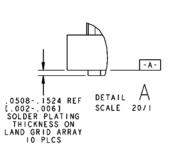
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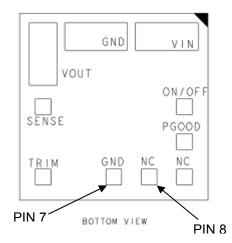
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### **Mechanical Outline**









PIN	FUNCTION
1	ON/OFF
2	VIN
3	GND
4	VOUT
5	SENSE
6	TRIM
7	GND
8	NC
9	NC
10	PGOOD

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs

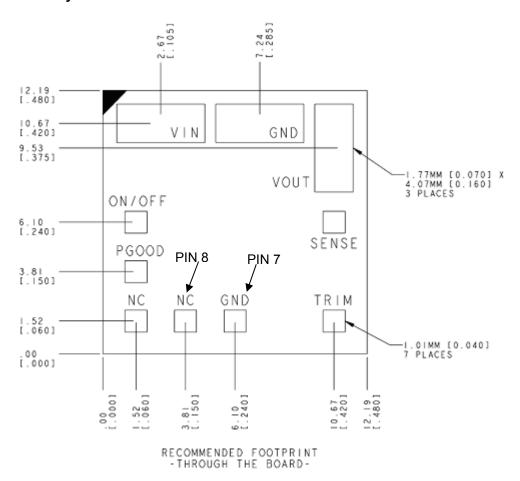


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#### **Mechanical Outline (continued)**

#### **Recommended Pad Layout**



Dimensions are in millimeters and (inches).

Tolerances: x.x mm  $\pm$  0.5 mm (x.xx in.  $\pm$  0.02 in.) [unless otherwise indicated] x.xx mm  $\pm$  0.25 mm (x.xxx in  $\pm$  0.010 in.)

Note: This module is recommended and compatible with Pb-Free Reflow Soldering and must be soldered using a reflow profile with a peak temperature of no more than 260°C for less than 5 seconds.

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



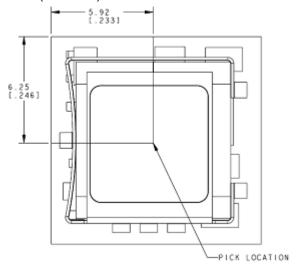
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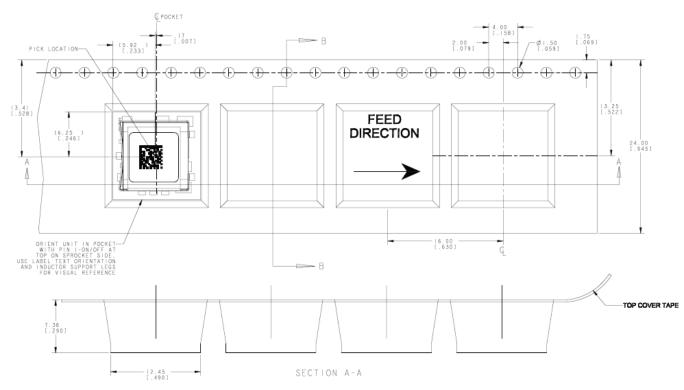
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## **Packaging Details**

The SLIN-03E2Ax modules are supplied in tape & reel as standard. Modules are shipped in quantities of 400 modules per reel.

All Dimensions are in millimeters and (in inches).





Reel Dimensions:

Outside Dimensions: 330.2 mm (13.00) Inside Dimensions: 177.8 mm (7.00") Tape Width: 24.00 mm (0.945")

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



October 8, 2009 Surface Mount Information Bel Power, Inc., a subsidiary of Bel Fuse, Inc.

#### **Pick and Place**

The SLIN-03E2Ax modules use an open frame construction and are designed for a fully automated assembly process. The modules are fitted with a label designed to provide a large surface area for pick and place operations. The label meets all the requirements for surface mount processing, as well as safety standards, and is able to withstand reflow temperatures of up to 300°C. The label also carries product information such as part number and serial number.

#### **Nozzle Recommendations**

The module weight has been kept to a minimum by using open frame construction. Variables such as nozzle size, tip style, vacuum pressure and placement speed should be considered to optimize this process. The minimum recommended inside nozzle diameter for reliable operation is 3mm. The maximum nozzle outer diameter, which will safely fit within the allowable component spacing, is 7 mm.

#### **Lead Free Soldering**

The SLIN-03E2Ax modules are lead-free (Pb-free) and RoHS compliant and fully compatible in a Pb-free soldering process. Failure to observe the instructions below may result in the failure of or cause damage to the modules and can adversely affect long-term reliability.

#### **Pb-Free Reflow Profile**

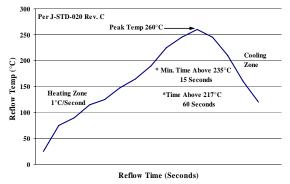
Power Systems will comply with J-STD-020 Rev. C (Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices) for both Pb-free solder profiles and MSL classification procedures. This standard provides a recommended forced-air-convection reflow profile based on the volume and thickness of the package. The suggested Pb-free solder paste is Sn/Ag/Cu (SAC). The recommended linear reflow profile using Sn/Ag/Cu solder is shown in the figure below. Soldering outside of the recommended profile requires testing to verify results and performance.

#### **MSL Rating**

The SLIN-03E2Ax modules have a MSL rating of 2.

#### Storage and Handling

The recommended storage environment and handling procedures for moisture-sensitive surface mount packages is detailed in J-STD-033 Rev. A (Handling, Packing, Shipping and Use of Moisture/Reflow Sensitive Surface Mount Devices). Moisture barrier bags (MBB) with desiccant are required for MSL ratings of 2 or greater. These sealed packages should not be broken until time of use. Once the original package is broken, the floor life of the product at conditions of  $\leq 30^{\circ}$ C and 60% relative humidity varies according to the MSL rating (see J-STD-033A). The shelf life for dry packed SMT packages will be a minimum of 12 months from the bag seal date, when stored at the following conditions:  $< 40^{\circ}$  C, < 90% relative humidity.



Recommended linear reflow profile using Sn/Ag/Cu solder.

4.5 Vdc - 14 Vdc Input, 0.59 Vdc - 5.5 Vdc /3 A Outputs



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#### **Post Solder Cleaning and Drying Considerations**

Post solder cleaning is usually the final circuit-board assembly process prior to electrical board testing. The result of inadequate cleaning and drying can affect both the reliability of a power module and the testability of the finished circuit-board assembly.

### **Revision History**

Date	Revision	Changes Detail	Approval
2009-6-10	Α	First release	T. Bubriski
2009-6-25	В	Section "Surface Mount Information" added.	T. Bubriski
2009-9-1	С	Tunable Loop <sup>™</sup> logo added.	T. Bubriski
2009-10-8	D	Updated Startup Time waveforms on page 10.	T. Bubriski

### **RoHS Compliance**

Complies with the European Directive 2002/95/EC, calling for the elimination of lead and other hazardous substances from electronic products.



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